

<b>Notice of References Cited</b>	Application/Control No. 10/808,343	Applicant(s)/Patent Under Reexamination MURANAKA, KOJI	
	Examiner Tuan Quach	Art Unit 2826	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,329,062	12-2001	Gaynor, Justin F.	428/447
	B	US-2003/0031877	02-2003	Veerasamy et al.	428/426
	C	US-4,714,636	12-1987	Yokono et al.	428/1.31
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, S., Silicon Processing for the VLSI Era, Vol. 2, Lattice Press, 1990, pp. 273-283.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.